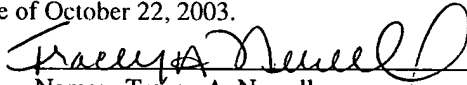


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	:		
Hiroyuki TAKAHASHI	:		
	:		
	:		
Appln. No.:	:	Art Unit:	To be assigned
To be assigned	:		
	:		
Filed:	:	Examiner:	To be assigned
October 22, 2003	:		
	:		
For: SEMICONDUCTOR MEMORY DEVICE	:	Docket No.:	KAM-01001
AND CONTROL METHOD THEREOF	:		

Certificate of Express Mailing

I hereby certify that the foregoing documents are being deposited with the United States Postal Service as Express Mail, postage prepaid, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this date of October 22, 2003.


Name: Tracey A. Newell
Express Mail Label: EV 325286086 US

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith on Form PTO-1449 is a listing of documents known to Applicants and/or their attorney in compliance with the requirements of 37 C.F.R. 1.56. Copies of the documents are also being submitted.

Since the above-referenced application is being filed on even date herewith, no fee is due. References AL, AM, AN, listed on the attached form 1449, are Japanese language publications which are discussed in the present application. Reference AR is an English language publication which is also discussed in the present application.

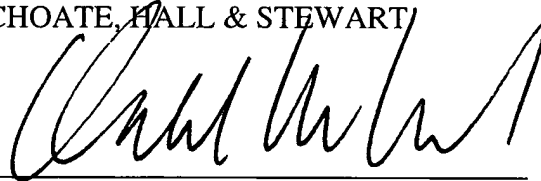
The Examiner is respectfully requested to initial the space adjacent to each document on the PTO-1449 form and return a copy of the PTO-1449 form to confirm that these documents have been considered by the Examiner and made of record in this application.

Although we believe that we have appropriately provided for any fees due in connection with this submission, the Commissioner is authorized to credit any overpayment or charge any deficiencies to/from our **Deposit Account No. 031721**. Two originally-executed copies of this form are being submitted.

Should there be any questions after reviewing this paper, the Examiner is invited to contact the undersigned at (617) 248-4038.

Respectfully submitted,

CHOATE, HALL & STEWART



October 22, 2003

Date

Donald W. Muirhead
Registration No. 33,978

Patent Group
Choate, Hall & Stewart
Exchange Place
53 State Street
Boston, Massachusetts 02109

INFORMATION DISCLOSURE CITATION

IN AN APPLICATION

(Use several sheets if necessary)

Docket Number (Optional)

KAM-01001

Application No.

To be assigned

Applicant Hiroyuki TAKAHASHI

Filing Date October 22, 2003

Group Art Unit To be assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
	AL P2001-283587A	10/12/2001	JP				X
	AM A-3-263685	11/25/1991	JP				X
	AN 2653689	05/23/1997	JP				X
	AO						
	AP						
	AQ						

OTHER DOCUMENTS

(Including author, Title, Date, Pertinent Pages)

AR	Enhanced Memory Systems, Inc., Web Page Products News; Preliminary Datasheet, 72Mbit Pipelined BSRAM w/ NoBL Architecture 2Mx36 (30 pages) http://www.edram.com/products/datasheets/ss2625ds_rl.1.pdf (p. 6)
AS	
AT	
AU	
AV	
AW	
AX	
AY	
AZ	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.